

Automotive Transient Simulation Test System

LAS 7600 Series

Introduction:



LAS-7600 system is a comprehensive automotive electronic immunity test solution designed in accordance with ISO 7637-2/3 standards and guided by mainstream automakers' testing criteria. It is tailored for executing essential EMC (Electromagnetic Compatibility) tests in the automotive electronics industry. Beyond fully complying with ISO 7637 standards, this system integrates hardware and software seamlessly, enabling the simulation of thousands of interference waveforms specified by vehicle manufacturers through PC-based operation.

Standards:

ISO 7637-2 ISO 7637-3 ISO 16750-2 GB/T 21437.2 GB/T 21437.3 Automaker's standards

Application area:

Key Features for the Automotive Electronics Industry, Including OEMs and Automotive Component Suppliers

Technical characteristics:

- Pre-installed with various OEMs' testing standards across years, allowing direct access.
- ◆ Compliant with international standards such as ISO, SAE, DIN, JASO, and numerous automaker-specific standards worldwide.
- Capable of testing vehicles with 12V/24V/48V systems.
- Unique "Master-Slave" system architecture supports unlimited scalability of system functions.
- ◆ Remote control and operation via Windows-based software.
- ◆ AUTOPRO software enables automatic control of the LAS-7600 test system, including system monitoring & management, EUT (Equipment Under Test) monitoring, waveform editing, test plan formulation, and automated report generation.
- Single-terminal output simplifies setup, allowing for completion of all tests with a single configuration.
- Direct addition of test items within the root directory.
- ◆ Equipped with a 7-inch capacitive touchscreen control panel for enhanced anti-interference performance, ensuring compliance with EMC standards.



Parameters:

Model	VEP 7610	
Interference Types	P1 Waveform	P2a Waveform
Output Voltage (Us)	-1 V~ -700 V	1 V ~150 V
Output Resistance (Ri)	$2 \Omega, 4 \Omega, 10 \Omega, 30 \Omega, 50 \Omega$	
Pulse Width (Td)	50 μs, 200 μs, 300 μs, 500 μs, 1000 μs, 2000 μs	
Rise Time (Tr)	12 V;1 μs; 24 V;3 μs	1 μs
Model	VEP 7630	
Interference Types	P3a Waveform	P3b Waveform
Output Voltage (Us)	-10 V ~ -800 V	10 V ~ 800 V
Repetition Time (T1)	$5 \mu s \sim 2000 \mu s$	
Pulse Train Width (T4)	1 ms ~ 100 ms	
Model	VEP 7650 A/B	
System Compatibility	12 V System	24 V System
Output Voltage (Us)	10.0 V ~ 105.0 V	10.0 V~210.0 V
Output Resistance (Ri)	$0.5~\Omega \sim 8~\Omega$	$1 \Omega \sim 8 \Omega$
Pulse Width (Td)	40 ms, 100 ms, 200 ms, 350 ms, 400 ms (or continuously adjustable)	100 ms, 200 ms, 350 ms, 400 ms (or continuously adjustable)
Rise Time (Tr)	$5 \text{ ms} \sim 10 \text{ ms}$	
Suppression Voltage (Us*)	10.0 V~ 100.0 V	10.0 V~ 200.0 V
Model	VEP 7620	
Interference Types	Pulse 2b	Pulse 4
Battery Voltage (Ua)	0 V~ 40.00 V	0 V~ 40.00 V
Pulse Width (Td)	$0.1 \text{ s} \sim 5.0 \text{ s}$	1 ms ~ 999 ms
Rise Time (Tr)	$0.5 \text{ ms} \sim 10.0 \text{ ms}$	1 ms ~ 999 ms
Battery Fall Time (T12)	$0.5 \text{ ms} \sim 10.0 \text{ ms}$	1 ms ~ 999 ms
Wait Time (T6)	$0.5 \text{ ms} \sim 10.0 \text{ ms}$	0.1 s ~ 99.9 s
Common Parameters for All Models		
Power Supply	220 V, 50 Hz, 500 W	
CDN Capacity	80 V/100 A	
Optional Accessories	Capacitive Coupling Clamps, Current Injection Clamps, Direct Coupling Capacitors	